

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:

First Named Inventor: MURANAKA, MASAYA

**Art Unit: 2193**

Appln. No.: 10/588,788

**Examiner: NGO, CHUONG D.**

Filed: August 9, 2006

**Confirmation No.: 2773**

For: RANDOM NUMBER GENERATING METHOD AND SEMICONDUCTOR  
INTEGRATED CIRCUIT DEVICE

\* \* \*

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the documents listed on the attached Form PTO/SB/08A are hereby cited.

Documents 1, 2, and 7 were cited in a counterpart European application (copy of Supplementary European Search Report submitted herewith). Documents 6, 8, and 9 were cited in a counterpart Japanese application (copy of Japanese Office Action and English translation submitted herewith). Documents 3-5 are U.S. counterparts of Document 9.

The Commissioner is hereby authorized to charge to Deposit Account No. 50-1165 (XA-10628) any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper and to credit any overpayment to that Account. If any extension of time is required in connection with the filing of this paper and has not been separately requested, such extension is hereby requested.

Respectfully submitted,

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